

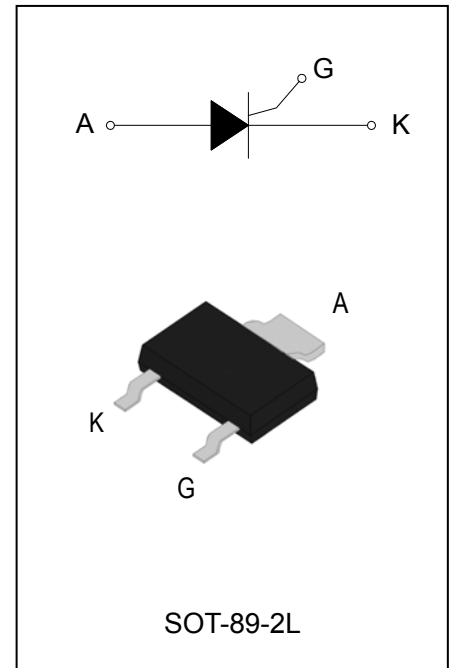
## SCRs

### General Description

Sensitive triggering SCR is suitable for the application where gate current limited such as small motor control, Earth leakage circuit breakers or Ground Fault Circuit Interrupters (GFCI), Solid state relays, General purpose switching, Small engine ignition.

### Features

- ◆ Repetitive Peak Off-State Voltage : 800V
- ◆ R.M.S On-State Current (  $I_{T(RMS)}$  )= 0.8 A )
- ◆ These are Pb-Free Devices



### Absolute Maximum Ratings

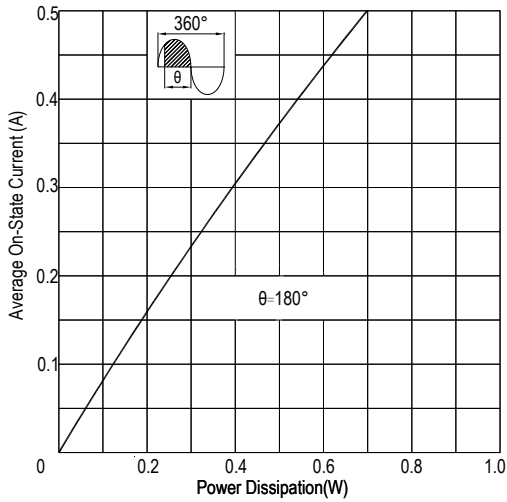
Symbol	Items	Conditions		Ratings	Unit
$V_{DRM}$ $V_{RRM}$	Repetitive Peak Off-State Voltage Repetitive peak reverse voltage	$T_j=25^\circ\text{C}$	MCR100-6A	800	V
$I_{T(AV)}$	Average On-State Current	Half Sine Wave , $T_c = 70^\circ\text{C}$		0.5	A
$I_{T(RMS)}$	R.M.S On-State Current	Half Sine Wave , $T_c = 70^\circ\text{C}$		0.8	A
$I_{TSM}$	Surge On-State Current	1/2 Cycle, Sine Wave Non-Repetitive, $t_p=10\text{ms}(50\text{Hz})$		8	A
$di/dt$	Critical rate of rise of on-state current	$T_j = 110^\circ\text{C}$ , $t_r \leq 100\text{ns}$		50	A/ $\mu\text{s}$
$I^2t$	$I^2t$ for Fusing	$T_j = 25^\circ\text{C}$ , $t_p = 10\text{ms}$		0.32	A <sup>2</sup> S
$P_{GM}$	Forward Peak Gate Power Dissipation	$T_j = 110^\circ\text{C}$ , Pulse Width $\leq 1.0\mu\text{s}$		0.5	W
$P_{G(AV)}$	Forward Average Gate Power Dissipation	$T_j = 25^\circ\text{C}$ , $t_p = 10\text{ms}$		0.1	W
$I_{GM}$	Peak Gate Current	$T_j = 110^\circ\text{C}$ , Pulse Width $\leq 1.0\mu\text{s}$		0.2	A
$T_j$	Operating Junction Temperature			- 40 ~ 110	$^\circ\text{C}$
$T_{STG}$	Storage Temperature			- 40 ~ 150	$^\circ\text{C}$



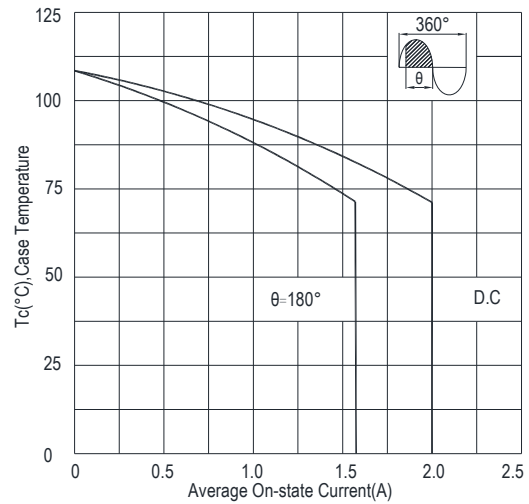
## Electrical Characteristics ( $T_j = 25^\circ\text{C}$ unless otherwise specified )

Symbol	Items	Conditions		MCR100-6A	Unit
$I_{DRM}$ $I_{RRM}$	Peak Forward Reverse	$V_{DRM} = V_{RRM}$ , $R_{GK} = 1K\Omega$ $T_j = 25^\circ\text{C}$	Max.	5	$\mu\text{A}$
	Blocking Current	$V_{DRM} = V_{RRM}$ , $R_{GK} = 1K\Omega$ $T_j = 110^\circ\text{C}$		0.1	mA
$V_{TM}$	Peak On-State Voltage	$I_{TM} = 1.0\text{A}$ , $t_p = 380\mu\text{s}$	Max.	1.5	V
$V_{GD}$	Non-Trigger Gate Voltage	$V_D = V_{DRM}$ $R_L = 3.3\text{k}\Omega$ $R_{GK} = 1K\Omega$ $T_j = 110^\circ\text{C}$	Min.	0.2	V
$V_{GT}$	Gate Trigger Voltage	$V_D = 6\text{V}$ , $R_L = 100\Omega$	Max.	0.8	V
$I_{GT}$	Gate Trigger Current		Max.	200	$\mu\text{A}$
$I_H$	Holding Current	$I_T = 0.05\text{A}$ $R_{GK} = 1K\Omega$	Max.	5	mA
$I_L$	Latching Current	$I_G = 1\text{mA}$ $R_{GK} = 1K\Omega$	Max.	6	mA
dV/dt	Critical Rate of Rise of Off-State Voltage	$V_D = 2/3V_{DRM}$ gate open $R_{GK} = 1K\Omega$ $T_j = 110^\circ\text{C}$	Min.	20	V/ $\mu\text{s}$
$R_{th(j-c)}$	Junction to case		Max.	38	$^\circ\text{C/W}$
$R_{th(j-a)}$	Junction to ambient		Max.	100	$^\circ\text{C/W}$

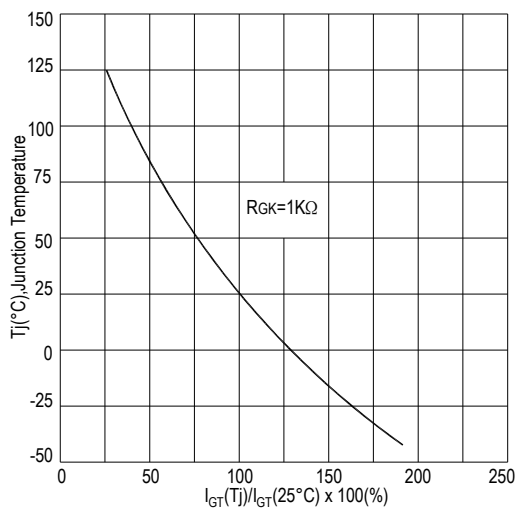
**FIG.1: Maximum average power dissipation (Single phase half wave)**



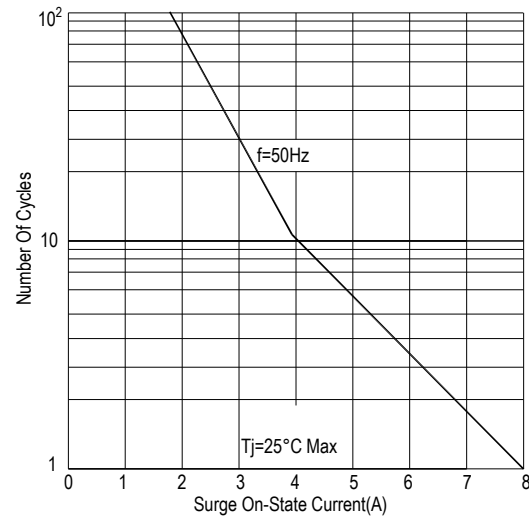
**FIG.2: Average on-state current VS Allowable case Temperature(Single phase half wave)**



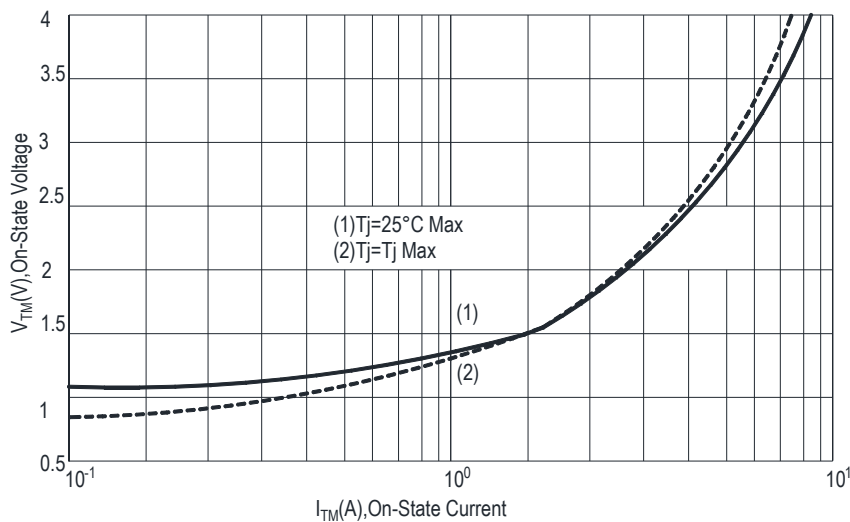
**FIG.3: Gate trigger current VS Junction temperature**



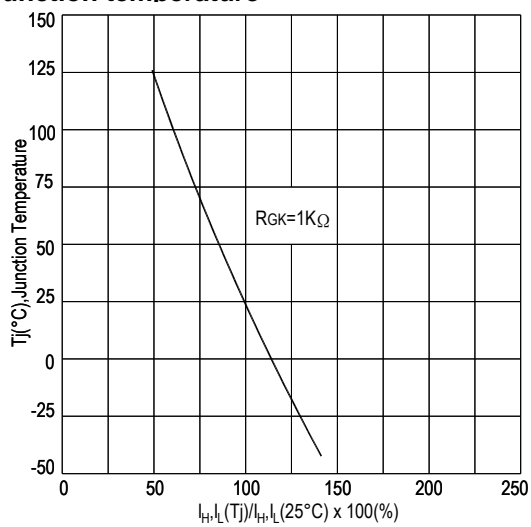
**FIG.4: Rated surge on-state current (Non-Repetitive)**



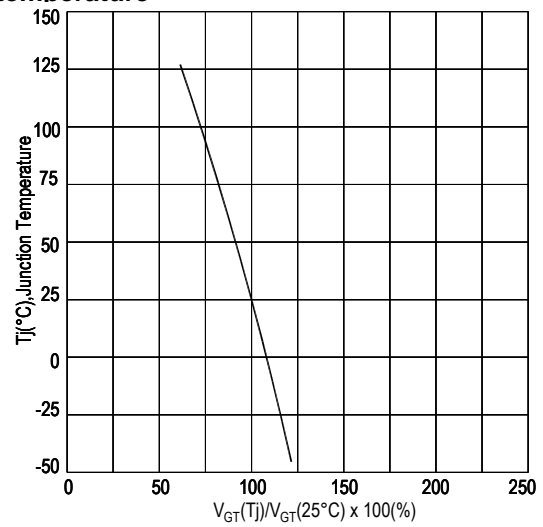
**FIG.5: On-state characteristics(Max)**



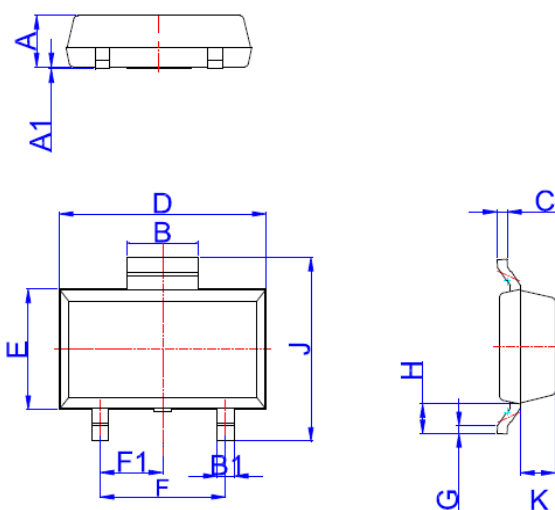
**FIG.6: Holding current and Latching current VS Junction temperature**



**FIG.7: Gate trigger voltage VS Junction temperature**



**PACKAGE MECHANICAL DATA**  
**SOT-89-2L Package Dimension**



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.300	1.500	0.051	0.059
A1	0.010	0.100	0.001	0.004
B	1.600	1.800	0.063	0.071
B1	0.300	0.500	0.012	0.020
C	0.220	0.320	0.009	0.013
D	4.750	5.150	0.187	0.203
E	2.750	3.150	0.108	0.124
F	3.000 TYP.		0.118 TYP.	
F1	1.500 TYP.		0.059 TYP.	
G	0.200	0.400	0.008	0.016
K	0.880 TYP.		0.035 TYP.	
H	0.580	0.980	0.023	0.039
J	4.300	4.700	0.169	0.185

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